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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>		Application Number	10/645,042
		Filing Date	
		First Named Inventor	SEUNG-MOON YOO
		Art Unit	
		Examiner Name	
Sheet 1 of 1	Attorney Docket Number	AUS920030534US1	

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
WAO		ANIS, M.H. et. al, "Energy-Efficient noise-Tolerant Dynamic Styles for Scaled-Down CMOS and MTCMOS Technologies", IEEE Transactions on Very Large Scale Integration (VLSI) Systems, April 2002, pp. 71-78, v. 10, n. 2, IEEE, United States (Publisher Item Id. S 1063-8210(02)00485-7).	

Examiner Signature		Date Considered	11/9/04
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.
¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.
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